
Table SPECCONN Datafill

The first two lines of programming is for a circuit that starts on DTC 0 span 10 channel 24. It is connected to PDTC 1 span 5 channel 31. PDTC 1 spans 5 and 6 are patched together on a DSX panel. PDTC 1 span 6 channel 31 is connected to DTC 0 span 11 channel 24. This scenario simulates a data circuit entering SL-100 #1 on a T-1 and leaving on E-1. It arrives at SL-100 #2 on E-1 and leaves on a T-1.

The third line of programming is for a circuit that starts on LEN 1 1 1 0 B-channel #1 and leaves the SL-100 on DTC 0 span 10 channel 1.

DS1 DTC 0 10 24 D30 PDTC 1 5 31 CON ACTIVE
DS1 DTC 0 11 24 D30 PDTC 1 6 31 CON ACTIVE

ISLC 1 1 1 0 B1 DS1 DTC 0 10 1 CON ACTIVE